MB&P Docket No: 13527-RO/SEI

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WHAT IS CLAIMED IS:

A semiconductor device, comprising:

a substrate having a process surface;

a first contact and a second contact arranged on the substrate, a second

contact surface of the second contact being at a greater distance, in a

substrate-normal direction, from the substrate than a first contact surface of the first

contact:

a first conductor disposed in a first patterned metal plane and electrically

connected to the first contact surface; and

a second conductor disposed in a second patterned metal plane and

electrically connected to the second contact surface; wherein the second metal plane

is disposed at a greater distance, in the substrate-normal direction, from the

substrate than the first metal plane.

2. The semiconductor device of claim 1, wherein the second contact is an

emitter contact of a bipolar transistor, and the first contact is one of a base contact

and a collector contact of the bipolar transistor, or one of a source contact, a gate

contact, and a drain contact of a MOS transistor.

3. The semiconductor device of claim 1, wherein the first contact is connected to

the conductor of the first metal plane via a first contact hole which extends in the

substrate-normal direction and is filled with an electrically conductive contact-hole

filling material.

4. The semiconductor device of claim 3, wherein the second contact is

connected to the second conductor of the second metal plane via a second contact

hole, which extends in the substrate-normal direction and is filled with an electrically

conductive contact-hole filling material.

5. A method for fabricating a semiconductor device, comprising:

providing a substrate, having a process surface;

23

MB&P Docket No: 13527-RO/SEI

OC Docket No.: INFN/0061

Express Mail No.: EV335472074US

defining a first contact and a second contact on the substrate, a second

contact surface of the second contact being located at a greater distance, in a

substrate-normal direction from the substrate than a first contact surface of the first

contact;

electrically connecting the first contact to a first conductor, of a first patterned

metal plane; and

electrically connecting the second contact to a second conductor of a second

patterned metal plane, wherein the second metal plane is disposed at a greater

distance, in the substrate-normal direction, from the substrate than the first metal

plane.

6. The method of claim 5, wherein the second contact is an emitter contact of a

bipolar transistor, and the first contact is one of a base contact and a collector

contact of the bipolar transistor, or one of a source contact, a gate contact, and a

drain contact of a MOS transistor.

7. The method of claim 5, wherein electrically connecting the first contact

comprises:

defining a first contact hole, which ends at the first contact surface and

extends in the substrate-normal direction, in an insulator;

filling the contact hole with an electrically conductive contact-hole filling

material; and

defining the first conductor of the first metal plane disposed above the first

contact in the substrate-normal direction to electrically connect to the contact-hole

filling material.

8. The method of claim 7, wherein electrically connecting the second contact

comprises:

defining a second contact hole, which ends at the second contact surface and

extends in the substrate-normal direction, in an insulator;

filling the contact hole with an electrically conductive contact-hole filling

material; and

24

MB&P Docket No: 13527-RO/SEI

OC Docket No.: INFN/0061

Express Mail No.: EV335472074US

defining the second conductor of the second metal plane disposed above the second contact in the substrate-normal direction to electrically connect to the contact-hole filling material.

9. The method of claim 5, wherein during a process for electrically connecting the second contact to the conductor of the second metal plane, at least one other conductor of the first metal plane is electrically connected to another conductor of the second metal plane.

## 10. A semiconductor device, having

a substrate having a process surface;

a first contact and a second contact arranged on the substrate, a second contact surface of the second contact being at a greater distance, in the substrate-normal direction, from the substrate than a first contact surface of the first contact: and

a patterned metal plane wherein a first conductor and a second conductor are formed; wherein the first contact is electrically connected to the first conductor of the patterned metal plane via a contact hole, which extends in the substrate-normal direction and is filled with an electrically conductive contact-hole filling material, and wherein the second contact directly adjoins the second conductor of the patterned metal plane.

11. The semiconductor device of claim 10, wherein the second contact is an emitter contact of a bipolar transistor, and the first contact is one of a base contact and a collector contact of the bipolar transistor, or one of a source contact, a gate contact, and a drain contact of a MOS transistor.

## 12. A method for fabricating a semiconductor device, comprising:

providing a substrate having a process;

defining a first contact and a second contact on the substrate, with a second contact surface of the second being at greater distance, in the substrate-normal direction, from the substrate than a first contact surface of the first contact;

MB&P Docket No: 13527-RO/SEI

OC Docket No.: INFN/0061

Express Mail No.: EV335472074US

electrically connecting the first contact to a first conductor of a patterned metal plane through a contact hole, which extends in the substrate-normal direction and is filled with electrically conductive contact-hole filling material; and

electrically connecting and adjoining the second contact to a second conductor of the patterned metal plane.

- 13. The method of claim 12, wherein the second contact is an emitter contact of a bipolar transistor, and the first contact is one of a base contact and a collector contact of the bipolar transistor, or one of a source contact, a gate contact, and a drain contact of a MOS transistor.
- The method of claim 12, wherein electrically connecting the first contact 14. comprises:

defining a first contact hole, which ends at the first contact and extends in the substrate-normal direction, in an insulator;

filling the first contact hole with an electrically conductive contact-hole filling material; and

defining the first conductor of the patterned metal plane to electrically connect to the contact-hole filling material.

15. The method of claims 12, wherein electrically connecting the second contact comprises:

defining the second contact surface, which is oriented in the substrate-normal direction, of the second contact utilizing a planar polishing step (CMP BPSG, CMP W); and

defining the second conductor of the patterned metal plane to adjoin the second contact surface of the second contact in an electrically conductive manner.